**FORM PTO-1449** ATTY. DOCKET NO. SERIAL NO. I-2-0135.2US 10/053,969 U.S. DEPARTMENT OF COMMERCE **APPLICANT** PATENT AND TRADEMARK OFFICE Stephen E. Terry INFORMATION DISCLOSURE **FILING DATE GROUP** STATEMENT BY APPLICANT January 22, 2002 2662 (Use several sheets if necessary) **U.S. PATENT DOCUMENTS** EXAMINER DATE CLASS DOCUMENT NUMBER NAME INITIAL AL 4 6 3 1 12/1986 Irvin 0 2 6 દે દિવ્યા Technology Center 2600 2 AM 5 7 8 8 2 8 01/1994 Chao 4 5 7 2 9 5 0 03/1998 Wegrzyn AN 8 3 8 6 7 7 Kozaki et al. AO 5 11/1998 ΑP 6 0 9 1 7 2 5 07/2000 Cheriton et al. 7 6 1 5 0 0 AQ 0 08/2000 Gaylord 5 AR 6 1 2 2 2 3 09/2000 Jones 2 4 AS 6 4 6 2 4 07/2002 Galand et al. 3 12/2002 Takamichi AT 6 4 9 6 5 1 FOREIGN PATENT DOCUMENTS TRANSLATION DOCUMENT NUMBER DATE CLASS SUBCLASS COUNTRY NO 3 0 A2 EP AU 4 0 5 06/1991 Ornı 8 7 5 2 ĒΡ A۷ 7 1 A2 11/1998 OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.) Chao et al., "Queue Management with Multiple Delay and Loss Priorities for ATM Switches," IEEE International NW. AW Conference on Communications, 1994, ICC 94, SUPERCOMM. New Orleans, LA. May 1-5, 1994, vol. 2, pgs. 1184-1189. Liebeherr et al., "Priority Queue Schedulers with Approximate Sorting in Output-Buffered Switches," IEEE AX Journal on Selecte Areas in Communications, Charlottesville, VA. June 1999, vol. 17, iss. 6 pgs. 1127-1144 DATE CONSIDERED will

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	AJ TS 25.321 V2.0.1 MAC Protocol Specification, TSG-RAN Working Group 2 (Radio and Radio Layer 3), Berlin, Germany, May 25-28, 1999.													yer 2	
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